



PATENT 81751.0032

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Junichi KARASAWA et al.

Serial No: 10/092,393

Filed:

March 5, 2002

For:

SEMICONDUCTOR DEVICE.

MEMORY SYSTEM AND

**ELECTRONIC APPARATUS** 

STATUS REQUEST

Mail Stop Status Request Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Examiner: Fazli Erdem

2826

Art Unit:

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed

Mail Stop Status Request Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450, on

April 16, 2004 ひとんと び ひとりしらん

Signature

Kathy Browning

Name fathi

Regarding the above-identified patent application, a Notice of Allowance was mailed on July 15, 2003, with a due date of October 15, 2003. We filed a Request for Continued Examination (RCE) and an IDS in the Patent Office on September 15, 2003. The last correspondence received from the PTO was a postcard acknowledging receipt of the RCE on September 22, 2003, which we filed on September 15, 2003. No further papers have been received in this case.

Please send the undersigned a Status Report on this application.

Respectfully submitted, HOGAN & HARTSON L.L.P.

Date: April 16, 2004

500 South Grand Avenue Suite 1900

Los Angeles, California 90071

Telephone: 213 337-6700 Facsimile: 213 337-6701

By:

Dariush G. Adli

Registration No. 51,386 Attorney for Applicant(s)